

**Notice of Allowability**

Application No.

10/776,381

Applicant(s)

ARAM, FARBOD

Examiner

Art Unit

Khai M. Nguyen

2819

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 9/7/2005.
2. ☒ The allowed claim(s) is/are 1-29.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
    - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 9/23/2005
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

### DETAILED ACTION

1. Claims 1-29 are allowed.

The following is an examiner's statement of reasons for allowance:

For claims 1, 11, 13, and 21-23, none of the prior art of record discloses or suggests an apparatus that comprises the compensation circuit for the first and second amplifier stages, wherein the compensation circuit includes: the first capacitance having one end communicates with an input of the first amplifier stage; an amplifier having an input communicates with the other end of the first capacitance and having an output communicates with one end of the second capacitance; the other end of the second capacitance communicates with an input of the second amplifier stage; and an impedance communicates between the input of the first amplifier stage and the output of the second amplifier stage.

For claims 4-5, and 16-17, the prior art of record does not disclose or suggest an apparatus that comprises the compensation circuit for the first and second amplifier stages, wherein the compensation circuit includes: the first capacitance having one end communicates with an input of the first amplifier stage; an amplifier having an input communicates with the other end of the first capacitance and having an output communicates with one end of the second capacitance; and the other end of the second capacitance communicates with an input of the second amplifier stage, wherein the output of the amplifier including the current source and transimpedance amplifier.

For claims 6, 2-3, 7-10, 12, 18, 14-15, 19, and 20, the prior art of record also fails to disclose or suggest an apparatus comprising: a compensation circuit for first and second amplifier stages, wherein the compensation circuit includes: a first capacitance having one end communicates with an input of the first amplifier stage; an amplifier having an input communicates with the other end of the first capacitance and having an output communicates with one end of a second capacitance; the other end of the second capacitance communicates with an input of the second amplifier stage; and a broadband buffer having one end that communicates with the output of the amplifier and the other end communicates with one end of the second capacitance.

And for claims 24-29, the primary reason for allowance of the claimed invention is the inclusion of, inter alia, the feedback capacitance having one end that communicates with the first terminal of the first transistor and the other end communicates with the control terminals of the second and third transistors. Because of the prior art fails to disclose or teach the claimed invention of the above claims, claims 1-29 are allowed.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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**Contact Information**

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khai M. Nguyen whose telephone number is 571-272-1809. The examiner can normally be reached on 9:00 - 5:30 Monday-Friday.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert (Bob) J. Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 28, 2005

Khai M. Nguyen  
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571-272-1809

  
PEGUY JEAN PIERRE  
PRIMARY EXAMINER